



OPENRAN

This Project Group Charter establishes the scope, intellectual property and copyright terms used to develop the materials identified in this Project Group. Only Participants that execute this Working Group Charter will be bound by its terms and be permitted to participate in this Project Group and shall be considered “Contributors” in the Project Group as defined in the **Telecom Infra Project IPR Policy document**.

TIP Board of Directors Approval Date: 11/1/2017

1. PROJECT GROUP NAME

OpenRAN – Enabling Open Ecosystem of GPP-Based RAN Solutions

2. PURPOSE

This project will focus on implementation of RAN solutions that can be deployed on General Purpose Processing Platforms thus enabling a sustainable development stream. This project will help enable an open ecosystem of complete solutions and solution components that take advantage of the latest capabilities of General Purpose Processing Platforms, both at a software level and also using programmable offload mechanisms such as FPGAs. The main objective of this project is to have RAN solutions that benefit from the flexibility and pace of innovation that are associated with software-driven developments on fully programmable platforms are deployed as outcomes of the project group.

3. PROJECT GROUP SCOPE

The project scope will be complementary with other projects already at play in TIP and will focus on disaggregation of virtualized RAN solutions into different components and ensuring each individual component can be efficiently deployed on GPP platforms:

- i. Start with 4G RAN Solutions but looking forward also 5G at a later phase (e.g. when 3GPP 5G NR specification is complete)
- ii. Define a reference framework/software architecture that decomposes LTE eNB into individual blocks and components, with a primary focus on L1.
- iii. Identify the blocks/components that do not include any vendor-specific IP and generate optimized software implementations

- iv. Identify the components and/or algorithms that can benefit from offloading to programmable accelerators such as FPGA
- v. Define set of APIs to abstract underlying hardware resources
- vi. Identify minimum requirements for General Purpose Processing Platforms to ensure efficient performance
- vii. Identify specific requirements for Virtualized Environments (e.g. Real-Time)
- viii. Demonstrate the performance achieved on different types of GPP Platforms (e.g. with and without FPGA)
- ix. Demonstrate multivendor RAN Solutions, where each solution is comprised of 1 or more HW (Processor) vendor and 1 or more SW vendor
- x. Create prototypes that can be rolled into product design for scale deployments

4. PROJECT GROUP DELIVERABLES

- i. Reference framework/architecture for implementation of eNB stack on General Purpose Processing Platforms
- ii. Reference (and optimized) implementation of the basic building blocks and algorithms, both as software libraries and FPGA RTL
- iii. Hardware Abstraction Layer, including APIs, to abstract from application vendors the underlying hardware platform capabilities
- iv. Defined KPIs and traffic model as part of the reference implementation
- v. Orchestration framework to manage and provide operational capabilities
- vi. Carrier-grade lab proof-of-concept of multi-vendor open solutions
- vii. Field trials to commercial, championed by at least one Comms Service Provider, for at least one of the use cases - high density locations in indoor environments, rural broadband connectivity, and multi-operator/neutral host. TIP Lab trials within 6 months and Field trials within 12 months of accepted use case.
- viii. Benchmark report and TCO analysis with performance metrics for various types of configurations
- ix. Recommendation for minimum set of platform requirements (both HW and OS Layers)

5. PATENT LICENSING

The patent license for all Contributions, Draft Specifications and Final Specifications within this Project Group shall be:

[Check one box]

- RAND License Option**, as set forth in Section 5.2.1 of the Telecom Infra Project IPR Policy.
- Royalty-free License Option**, as set forth in Section 5.2.2 of the Telecom Infra Project IPR Policy.

6. FINAL DELIVERABLE COPYRIGHT LICENSING

Project Group agrees to grant the following copyright license for the Final Specification:

[Check one box]

- Creative Commons Copyright Attribution 4**, Each Project Group Contributor agrees that its Contributions are subject to the Creative Commons Attribution 4.0 International license - <http://creativecommons.org/licenses/by/4.0/legalcode>.
- Full Release of Copyright into the public domain**, Each Project Group Contributor agrees to release its Contributions to the public domain and waive all copyrights associated with them.

7. INITIAL PROJECT CHAMPIONS

Vodafone and Intel

8. CHAIR AND(OR) CO-CHAIR OF PROJECT GROUP

Chair

Andrew Dunkin, Vodafone

Co-Chair

Jianli Sun, Technical, Intel, jianli.sun@intel.com

Adnan Boustany, Marketing, Intel, adnan.boustany@intel.com

9. PARTICIPATION CRITERIA

- i. Fit of the proposed contributions to the project group scope
- ii. Technical fit of the proposed contributions to the architectural framework of the project group
- iii. Commitment to contribute a non-proprietary open solution or open extension / API
- iv. Productive interaction with a system integrator